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Sagantec, TSMC collaborate on Sagantec DFM-Fix

26/10/2007 **by Sourav Dutta**

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Sagantec and Taiwan Semiconductor Manufacturing Company (TSMC) are providing automated capabilities for lithography hotspot correction at advanced process technology nodes with DFM-Fix.

DFM-Fix is said to speed turnaround time by automatically addressing hotspots in all critical layers at all design levels, including key building blocks such as library, memory, IP and custom blocks. It also provides automated handling of post-implementation hotspots caused by boundary proximities and inter-level effects.

Currently available fixing flows address hotspots in higher metal layers and at the routing implementation level. With DFM-Fix, designers can correct hotspots in all layers, as well as hotspots caused by inter-cell placement or interaction between IP and routing. This automated DFM correction encompasses the whole chip, at every design level and for every mask layer.

TSMC and Sagantec tested DFM-Fix on multiple complete designs with hotspots in various layers. In all test cases, DFM-Fix automatically corrected most of the hotspots. The flow also proved time-efficient.

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About

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Sourav is an experienced journalist and a 3D artist. He lives in New Delhi, India.

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