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## Industry coalition seeks to advance DFM

[George Leopold](#)

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WASHINGTON — A group of chip makers and EDA tool vendors have formed a design-for-manufacturing (DFM) coalition intended to build on previous DFM efforts.

Founding members of the DFM Coalition include Cadence Design Systems, Freescale Semiconductor, IBM Corp., Ponte Solutions, Samsung, Sagantec, ST Microelectronics and Texas Instruments. Formation of the coalition was announced Tuesday (Sept. 18) by the [Silicon Integration Initiative \(Si2\)](#).

Si2 said Ponte is contributing model-based yield analysis technology to the effort while Blaze DFM will contribute a proposed standard for the interaction between chip design tools and lithography simulation engines.

"DFM parameters have to be added to existing design flow, and the approach has to be consistent and holistic," Steve Schulz, president and CEO of Si2, said in a statement announcing formation of the DFM Coalition.

The coalition recently released a [DFM dictionary](#) to help promote the chip design approach.

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