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Saturday, December 01, 2007

Sagantec and Time to Market Team to Offer Design Migration Solutions

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November 19, 2007 -- **Sagantec** and **Time To Market, Inc.** (TTM) today announced a collaboration to jointly deliver complete IC design migration solutions. This program offers a variety of unique strategies for re-implementation of existing designs to new process technologies. This joint program combines Sagantec's layout reuse, process migration, and layout acceleration tools with TTM's IC design expertise and services.

These design migration solutions include: migration of complete IC designs to new process technologies, migration of designs from aging fabrication lines to modern fabrication processes, and porting from one foundry to another. These solutions support digital, mixed-signal, custom, or system-on-chip (SOC) ICs. This program enables these companies to take advantage of the denser and lower power technologies, and the ability to utilize additional or alternate wafer production sources. Sagantec and TTM offer the technology, expertise and resources to perform these tasks in a timely and efficient manner.

[Go to the Sagantec, Inc. website to find additional information.](#)

E-mail [Sagantec, Inc.](#) for more information.

Read more about [Sagantec, Inc.](#) and [Time To Market, Inc. \(TTM\)](#) on SOCcentral.com

Keywords: Sagantec, Time To Market (TTM), design migration, ASIC design, system-on-chip, SoC, 191/24358 11/19/2007 45 5

Designer's Marketplace

New Tensilica Diamond Standard Processor Cores

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Grant Martin
Chief Scientist
Tensilica, Inc.

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